

	Class	Subclass
ISSUE CLASSIFICATION		

PATENT NUMBER

<p>O.I.P.E.</p> <p>SCANNED <u>17C</u> Q.A. <u>17D</u></p>	PATENT DATE
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APPLICATION NO. 09/530549	CONT/PRIOR D F	CLASS 700 703	SUBCLASS 22	ART UNIT 2763	EXAMINER <u>JONES</u>
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21-3 GARCIA-ORTEGA

TITLE

METHOD AND DEVICE FOR SYSTEM SIMULATION OF
MICROCONTROLLERS/MICROPROCESSORS AND CORRESPONDING PERIPHERAL
MODULES

PTO-2040
12/89

ISSUING CLASSIFICATION															
ORIGINAL					CROSS REFERENCE(S)										
CLASS				SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)									
INTERNATIONAL CLASSIFICATION															

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____		ISSUE FEE	
Amount Due			Date Paid	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	
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